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In re the Application of: KIMURA, Tokuharu, et al.

Group Art Unit: 2811

Serial No.: 10/764,486

Examiner: HU

Filed: January 27, 2004

P.T.O. Confirmation No.: 6268

For:

COMPOUND SEMICONDUCTOR DEVICE AND ITS MANUFACTURE

LETTER

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

April 24, 2006

Sir:

Supplemental to the response filed April 13, 2006, attached is a sworn Declaration of Accuracy of Translation in Lieu of Sworn Translation (37 C.F.R. 1.68) of priority certificate (JP 2003-017308).

The translation submitted herewith is to be placed in the U.S. Patent and Trademark Office file of this case.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

Enclosure: Declaration of Accuracy of Translation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



DECLARATION OF ACCURACY OF TRANSLATION IN LIEU OF SWORN TRANSLATION (37 C.F.R. 1.68)

The undersigned, having an office at

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certifies and declares that:

- (1) I am fully conversant both with the Japanese and English languages.
- (2) I have translated into English Japanese Patent Application Number 2003-017308 filed in Japan on January 27, 2003. A copy of said English translation is attached hereto.
- (3) The translation is, to the best of my knowledge and belief, an accurate translation from the Japanese into the English language.

The undersigned declares further that all statements made herein of his own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001, of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the matter with which this translation is used.

Date: 12 April 2006

Dr. Keishiro TAKAHASHI